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09/724,972

PATENT  
Conf. No. 7821

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Stephen M. Trimberger et al.  
Assignee: Xilinx, Inc.  
Title: Programmable Logic Device With Decryption And Structure For  
Preventing Design Relocation  
Serial No.: 09/724,972 Filing Date: 11/28/2000  
Examiner: Minh Dieu T. Nguyen Art Unit: 2137  
Docket No.: X-805-9 US Conf. No.: 7821

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

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6/14/06*  
In response to the Final Office Action mailed April 21, 2006, Applicants submit  
the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.